

AMENDMENTS TO THE ABSTRACT

Please amend the abstract with the following amended abstract.

The present invention relates to a semiconductor device and, more particularly, to a clock enable buffer to output a clock enable signal which enables a clock buffer to produce an internal clock signal. The A semiconductor device according to the present invention comprises: includes a clock buffer block for receiving and buffering an external clock signal and then outputting an internal clock in response is a second control signal; a clock enable buffer block, which is enabled by a buffer enable signal, for comparing a reference voltage having a constant potential with a clock enable buffer signal and then generating a first control signal; a clock enable signal timing control block for outputting the second control signal by passing the clock enable signal to enabling the clock buffer block in response to the buffer enable signal or by delaying the clock enable signal for a predetermined time; and a clock enable signal latch block circuit for generating enabling the clock enable buffer using the clock enable buffer the enable signal after a power-up signal is inputted.